

10

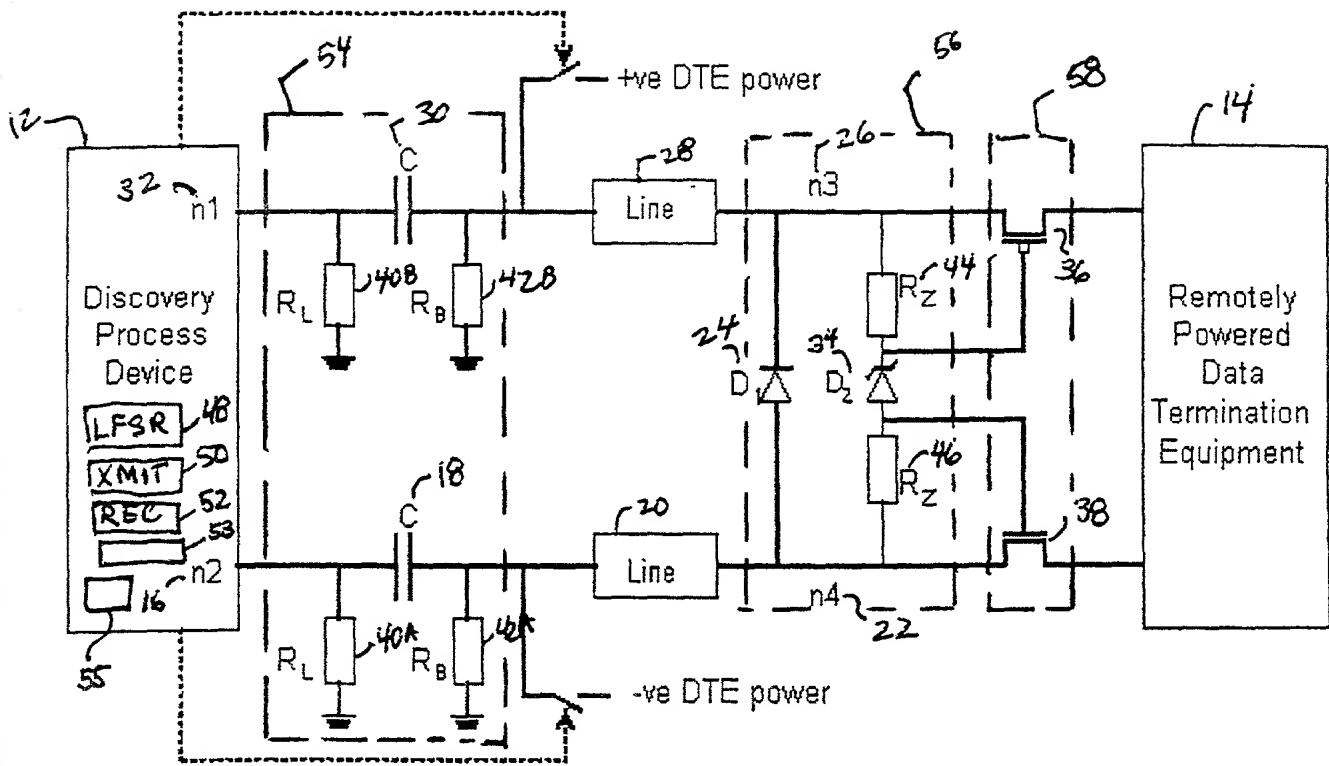
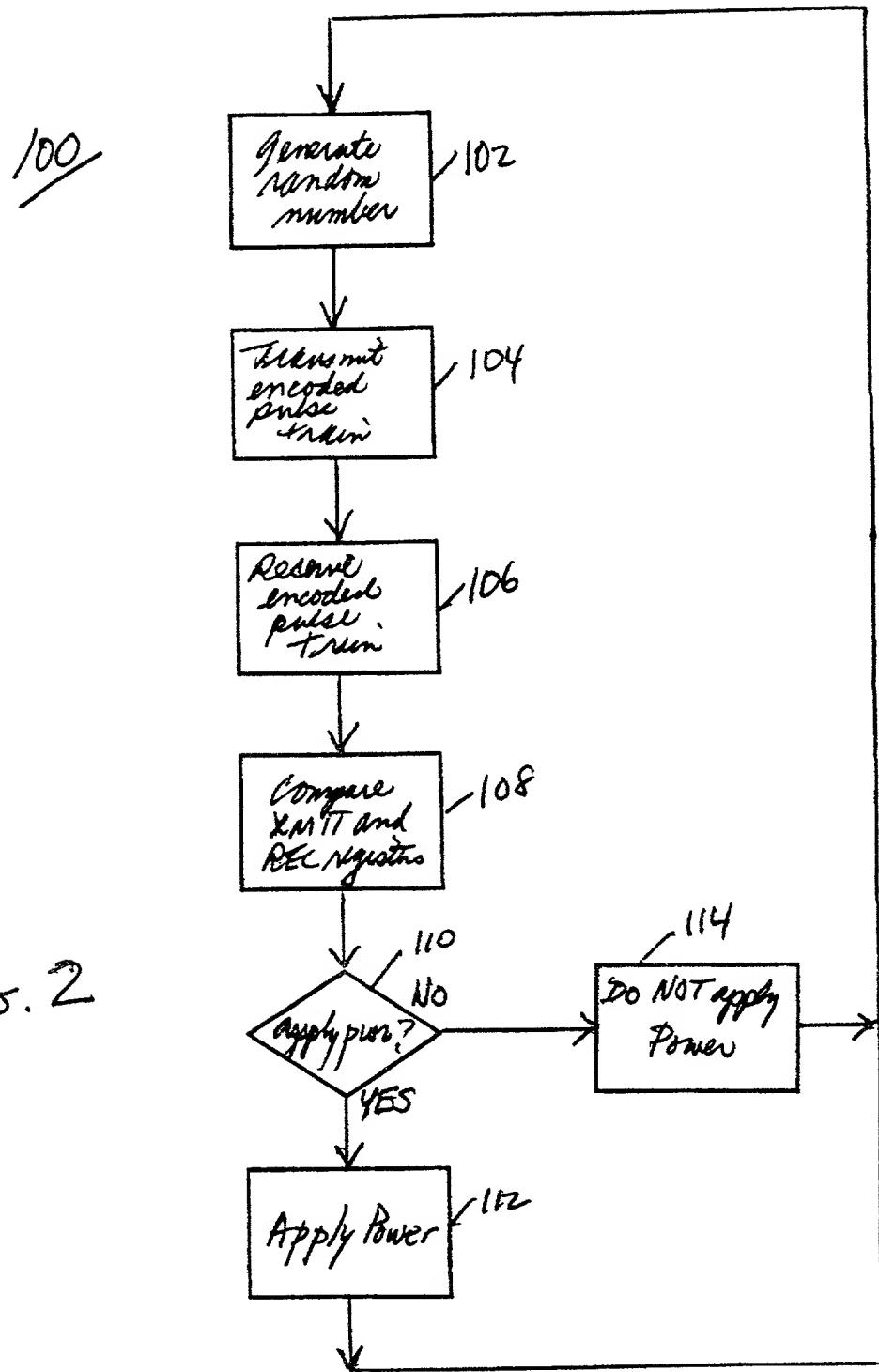
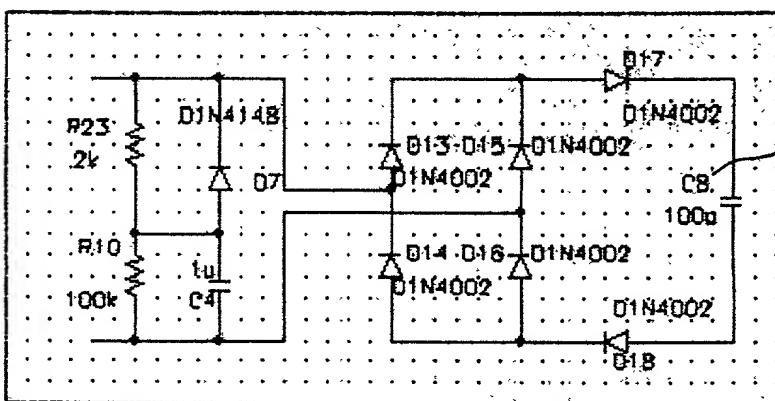
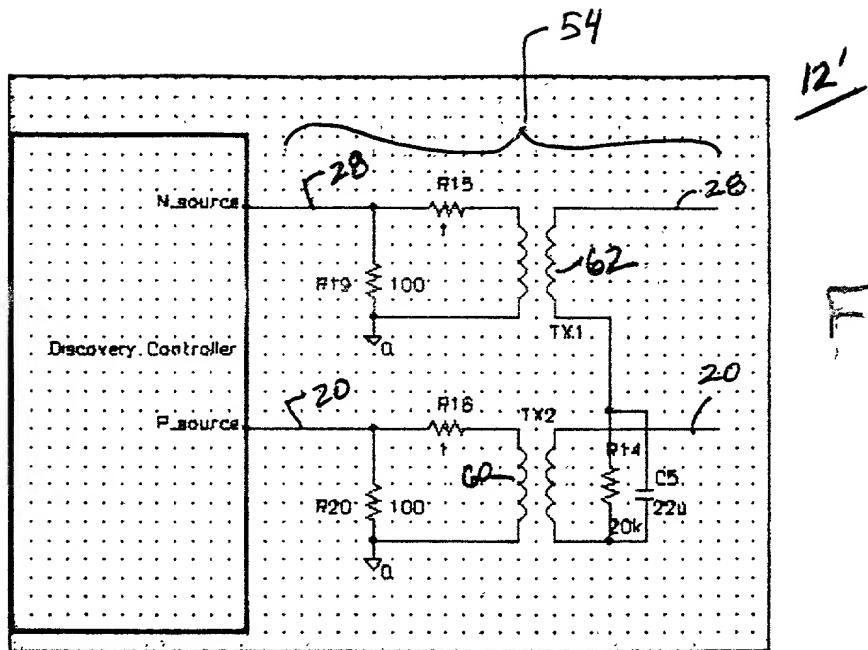


FIG. 1

FIG. 2





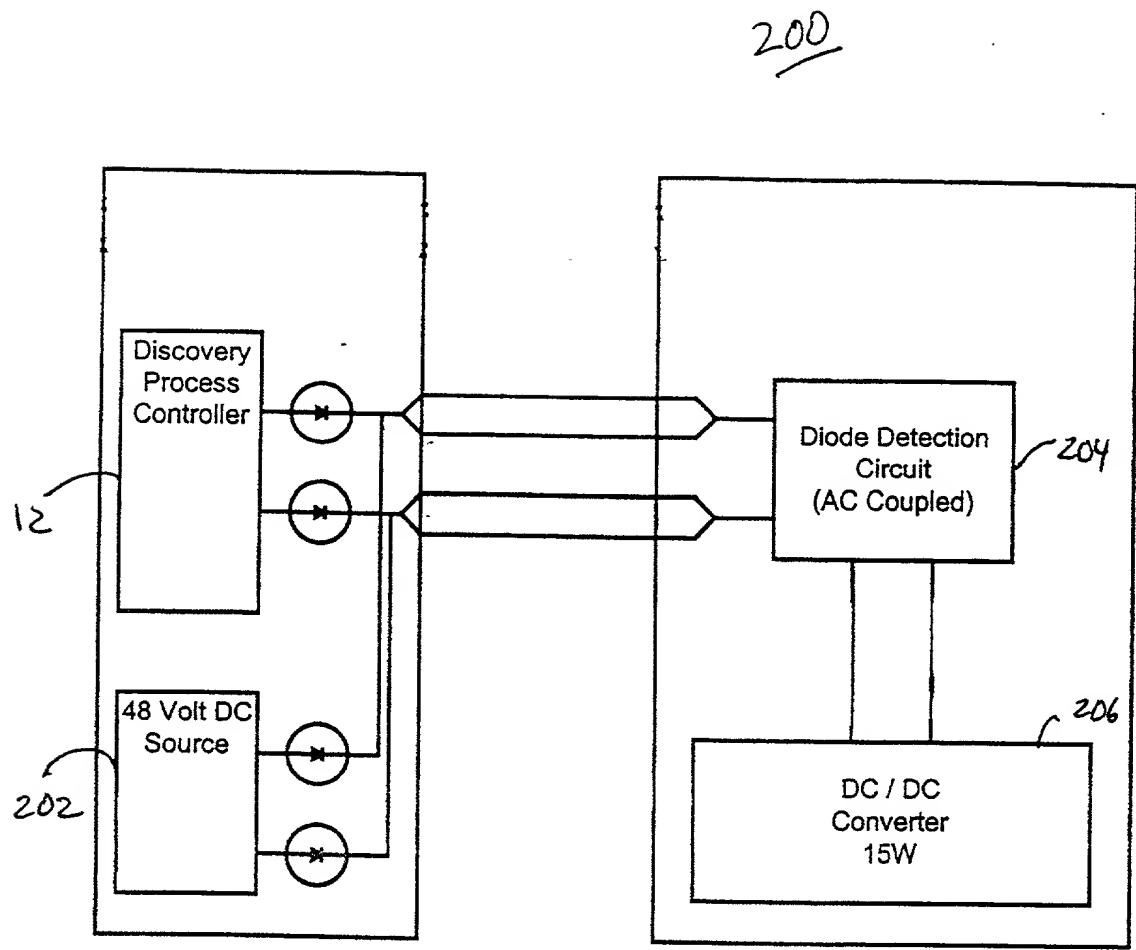
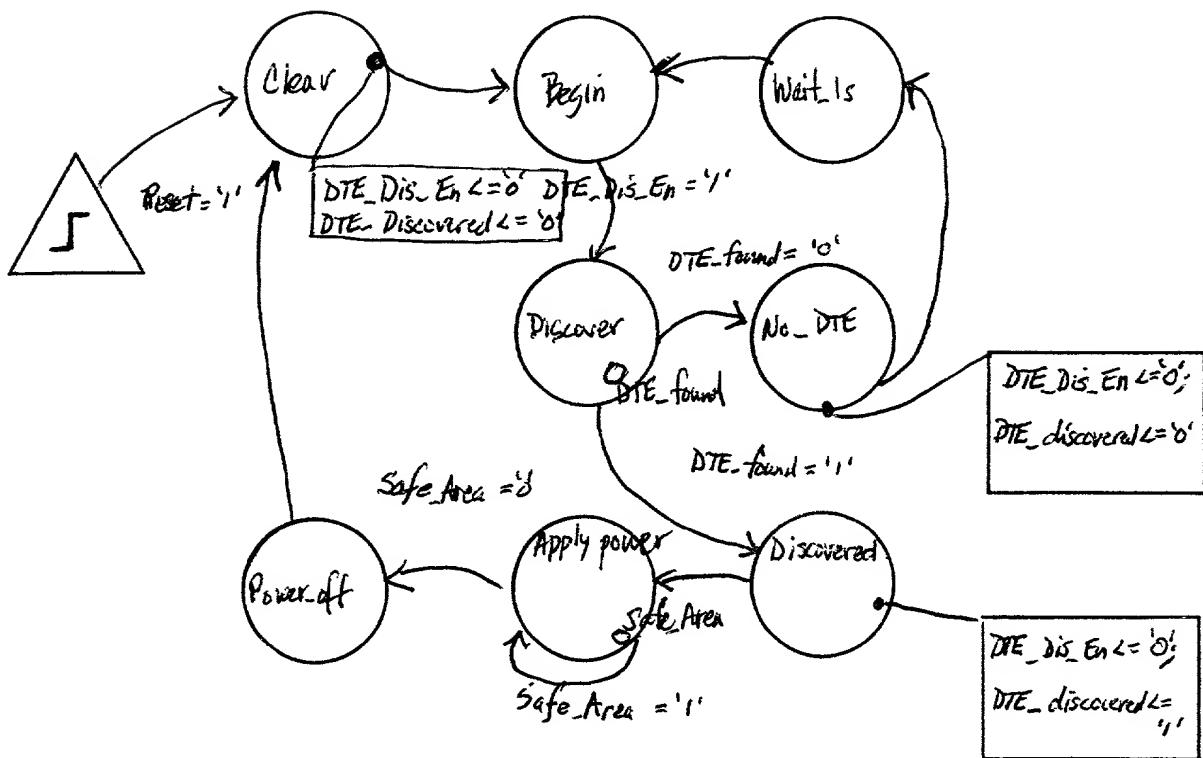


FIG. 5

250

$DTE_Dis_En = '0'$



F | G. 6

Power Level Detection Process

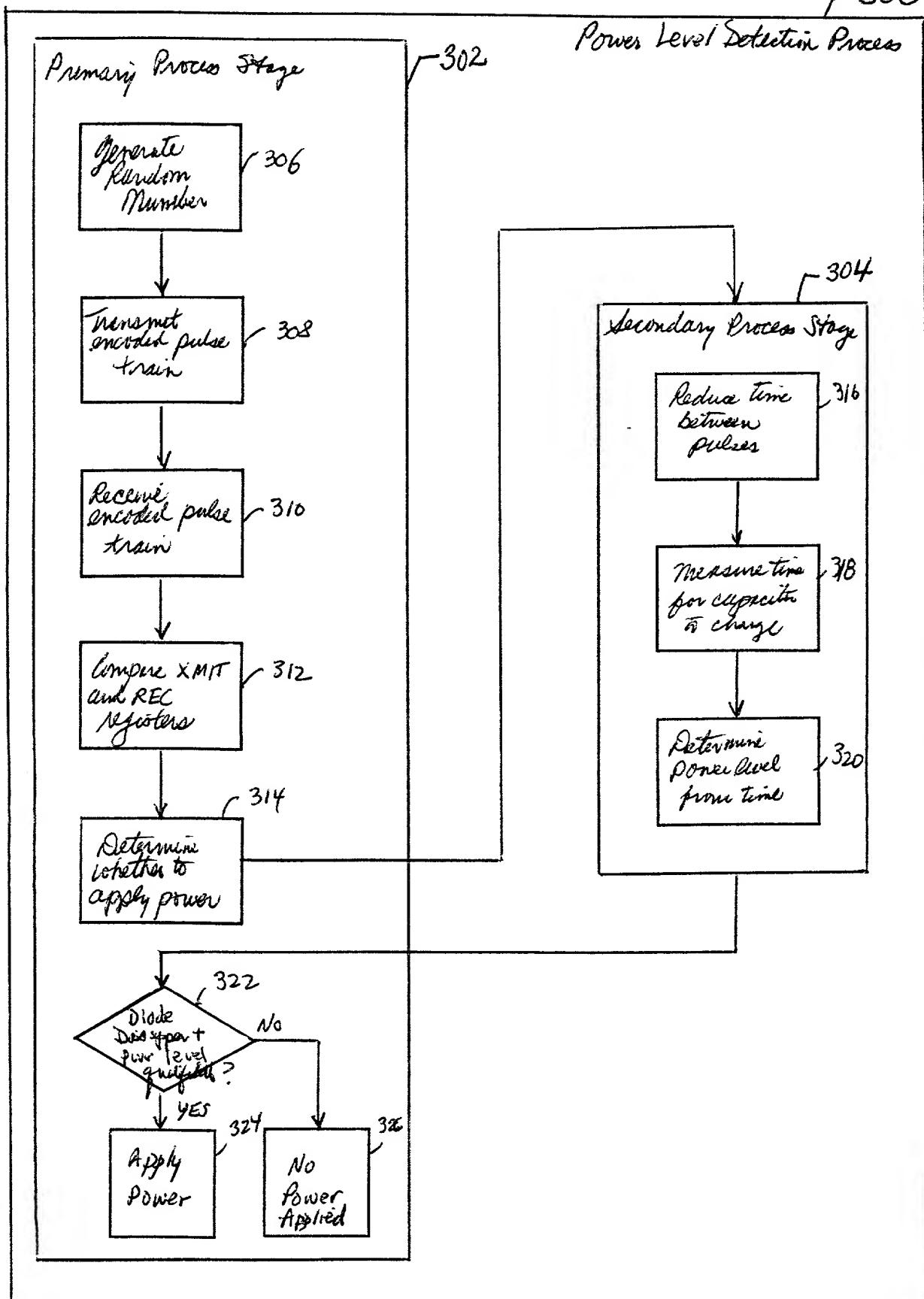


FIG. 7